

#### SPACE COMPONENTS NEWSLETTER September 2020

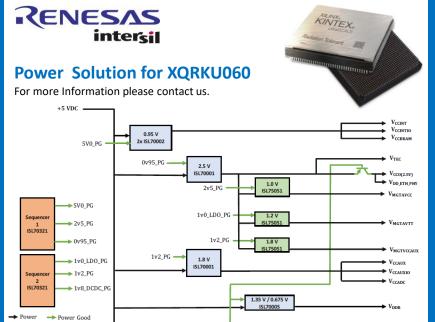


# Radiation Hardened 40V High Side Current Sense Amplifiers

The ISL70100SEH and ISL73100SEH are radiation hardened 40V current sense amplifiers built on the Renesas proprietary PR40 SOI process. These devices have a wide power supply range of 2.7V to 40V. The input common-mode voltage is independent of the supply voltage and extends from -0.3V to 40.0V, making them ideal to use in both high-side and low-side applications.

Learn More About ISL70100SEH >

Learn More About ISL73100SEH >



1v8\_DCDC\_PG

## Nano plore - RadHard FPGA News

- NXmap V3 Released
- Flight Heritage
  - NG-Medium FPGA (this September)
  - NG-Large expected 2021
- NX1H35AS (= NG-Medium Metal-fix) CQFP-352/CLGA-625 qualification running, according DLA & ESA instructions. Completion expected within coming weeks (SEP20).
- NG-Large Qualification started. Proto/EK leadtimes are currently 2wks BUT stock subjected to prior sale.
- NG-Ultra 1st space qualification expected 2021 on Organic package.



Q-Tech and Protec GmbH announce extended cooperation in United Kingdom and Ireland.

### EPC · SPACE

EPC & VPT Inc. announce Joint Venture
- EPC Space -

EPC Space, will provide advanced, high-reliability, gallium nitride (GaN) power conversion solutions for critical spaceborne and other high reliability environments, via Protec GmbH in DACH.





SVLFL and SVLTR Space-Qualified DC-DC Converters Now Available

Available in single and dual output voltages, both series are radiation-hardened to TID levels of 60 krad(Si) and Single Event Effects (SEE) performance to 85 MeV/mg/cm². The SVLFL 120W offers a wide input voltage range of 16 to 40V, plus 50V transient operation, and the SVLTR 40W has a wide input voltage range of 15 to 50V, plus 80V transient operation.



#### Space grade Interposer 1752 ways

Solderless connection solution. Interposer with IDI contacts between a PCB and a LGA components. Perfect solution for all space applications and components with High Pin count, like ASICs and FPGAs.

